

F-067

## PATENT ABSTRACTS OF JAPAN

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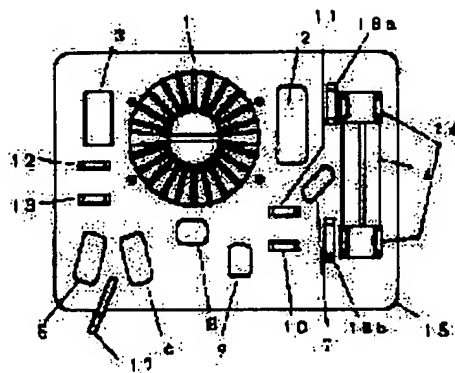
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## (54) TERMINAL NOISE FILTER

(57)Abstract:

**PROBLEM TO BE SOLVED:** To reduce a temperature rise at a fuse terminal and to obtain the excellent productivity and workability in the terminal noise filter to prevent an EMC fault due to a terminal noise.

**SOLUTION:** The terminal noise filter uses tab terminals 10, 11 to connect an electric wire to a printed circuit board 15 as a heat dissipation means. Thus, the terminals with excellent mount performance onto the printed circuit board and a flat shape are arranged up to the vicinity of a clip 14 requiring heat dissipation. Thus, since excellent high density mount performance is attained and the heat dissipation part is concentrated onto the vicinity of the heat generating part, high thermal conductivity is attained and a fuse terminal temperature is easily reduced.



## LEGAL STATUS

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**Analog low-pass filter with double sampling**

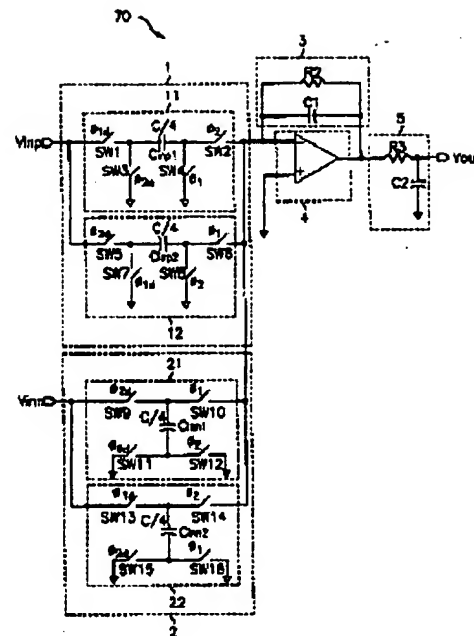
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**Inventor:** SHIN YUN TAE  
**Applicant:** HYUNDAI ELECTRONICS IND (KR)  
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**Abstract of FR2765417**

The filter (70) includes two terminals (1,2) which receive differential analog signals ( $V_{inP}$ ,  $V_{inN}$ ) which charge / discharge four capacitors ( $C_{inn1}$ ,  $C_{inn2}$ ,  $C_{inp1}$ ,  $C_{inp2}$ ). A differential amplifier (4) is connected to the common terminal of the capacitors. A filtering module (5) is connected between the amplifier asynchronous output and the filter output terminal. Switches (SW1-SW8) control the charge / discharge sequence of the capacitors. These switches are controlled by a clock signal.



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